Docket No.: A884SD (121116.00113)





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

icant: Microsemi Corp.

Application No.: 10/788,536

Filing Date: February 27, 2004

Title: PIN or NIP Low Capacitance Transient Voltage Suppressors and Steering Diodes

RESPONSE TO COMMUNICATIONS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

Date of Deposit: February 14, 2005. I hereby certify that this paper is being transmitted via U.S. Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450,

Alexandria, y 22313-1450.

Sir:

Pursuant to the Notice of Non-Compliant Amendment (37 CFR 1.121) dated January 19, 2005, enclosed herein please find Replacement Sheets 1-5 (Fig. 1A, Fig. 1B, Fig. 2A, Fig. 2B, Fig. 3A, Fig. 3B, Fig. 4A, Fig.4B, Fig. 4C, Fig. 5, Fig. 6, Fig. 7, Fig. 8, Fig. 9, Fig. 10, Fig. 11, Fig. 12, Fig. 13, and Fig. 14), for the above referenced case.

If you have any questions regarding this case please contact the undersigned attorney at the number below.

Respectfully submitted,

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